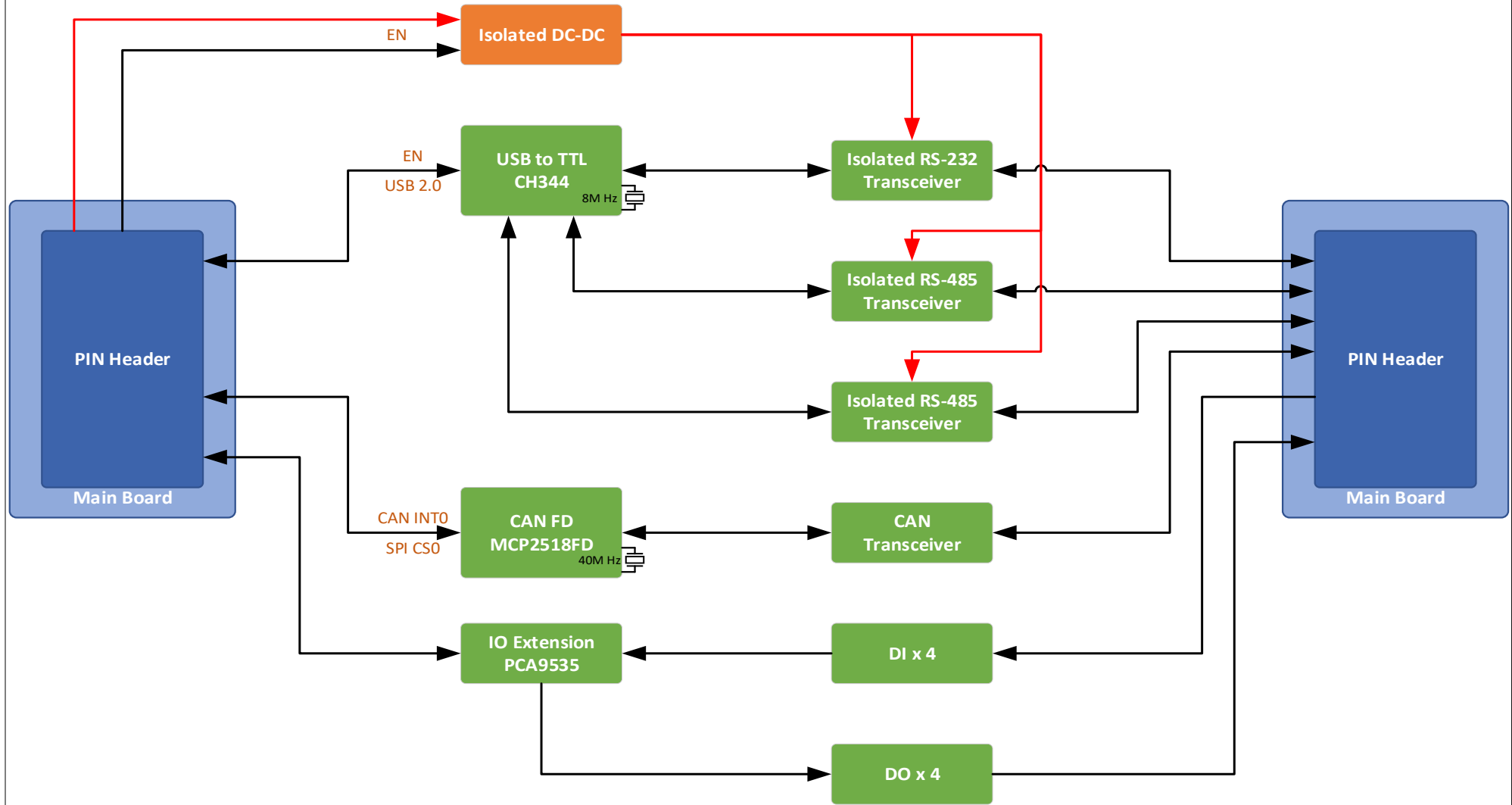
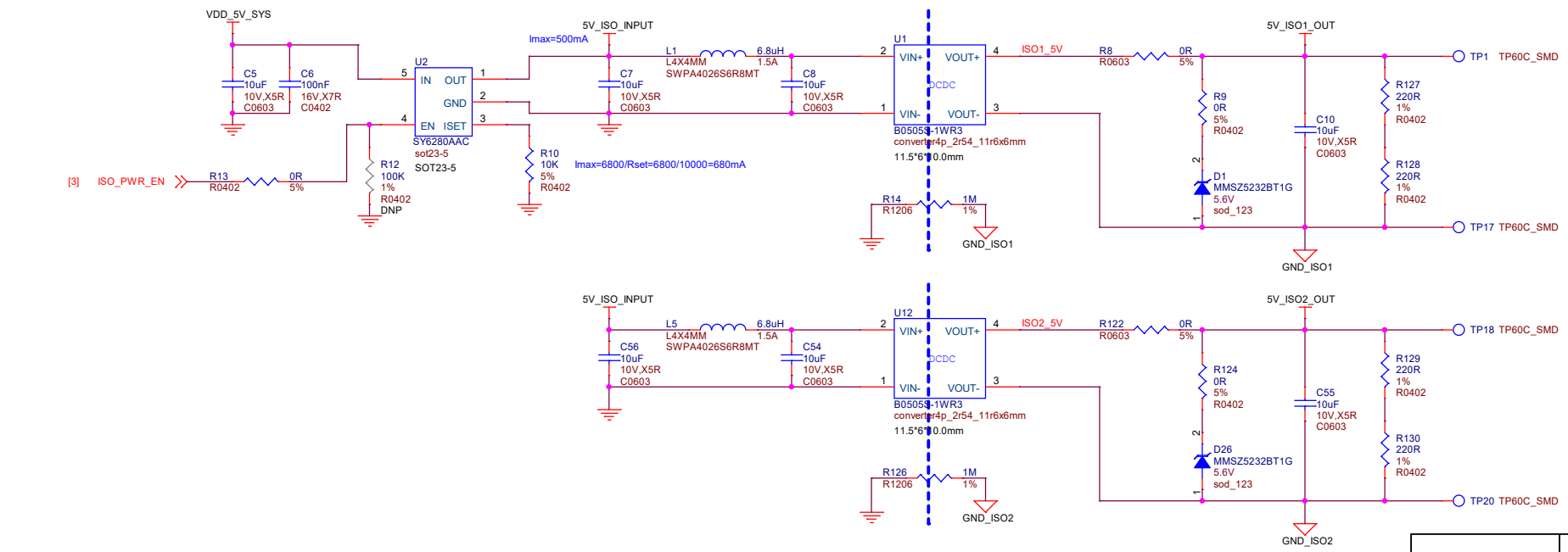
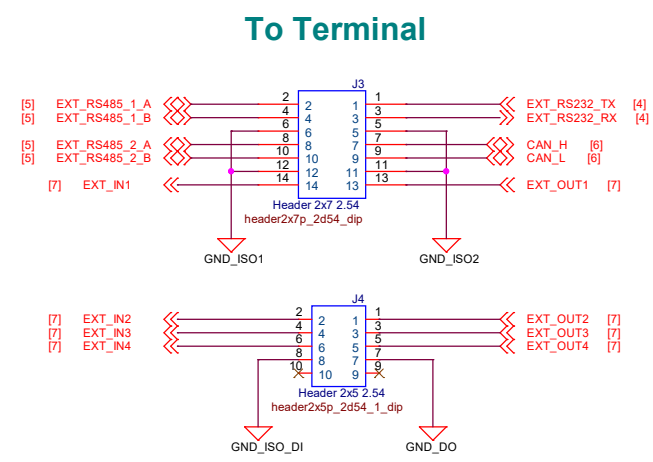
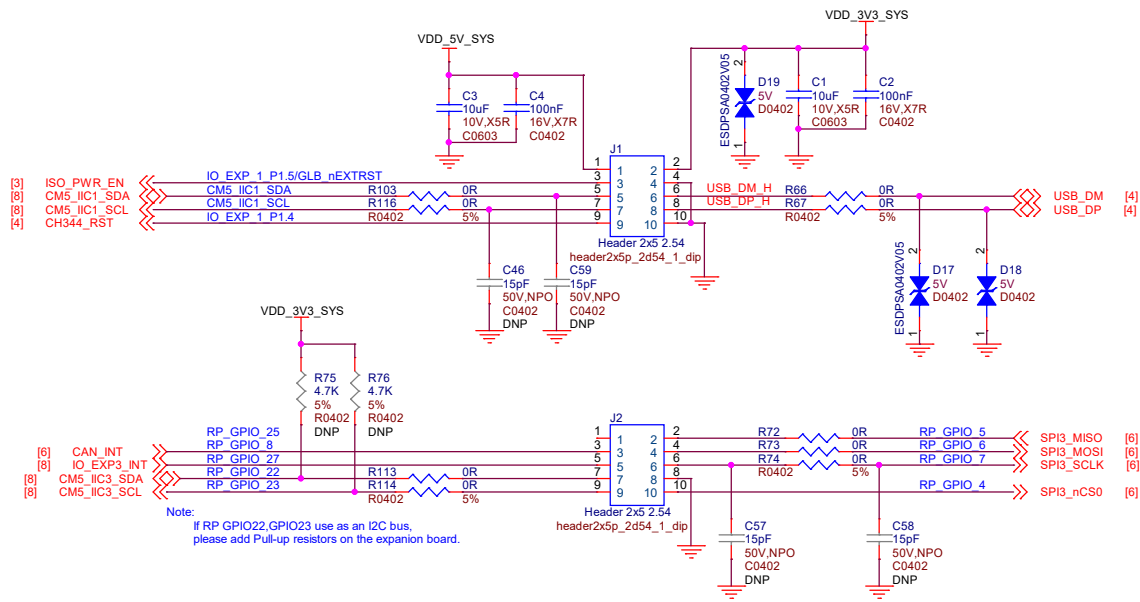


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REVISION HISTORY		
VER #	DATE	DESCRIPTION OF CHANGES
v1.0	2025.11.24	First release

Block Diagram

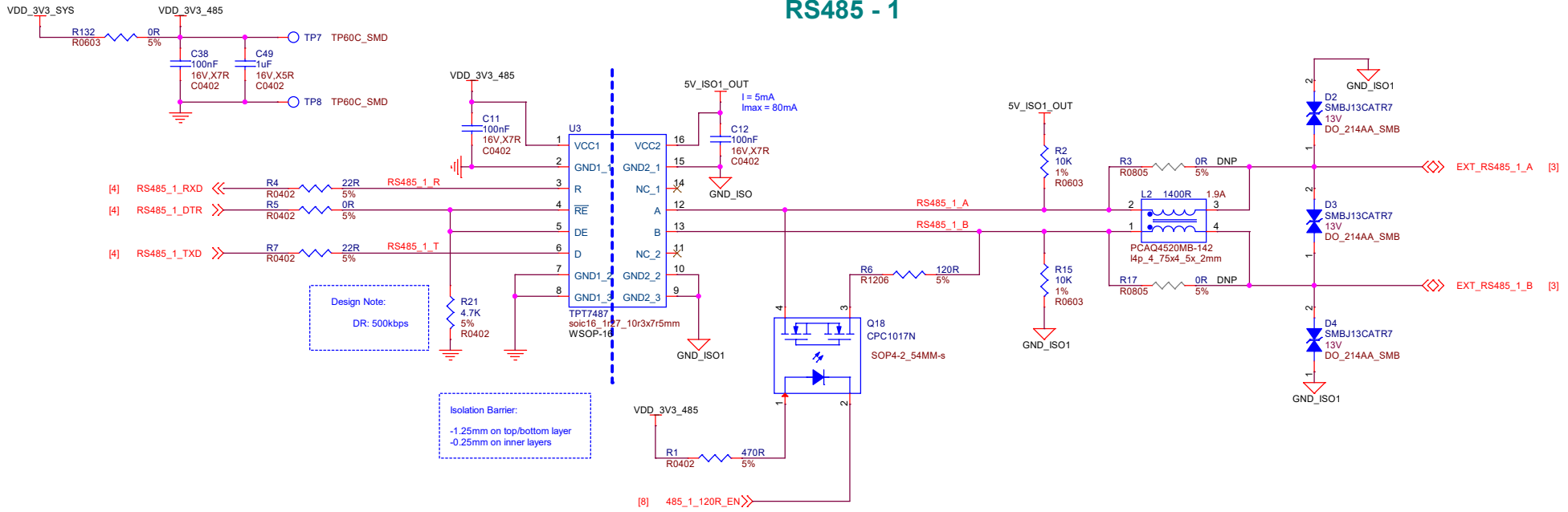




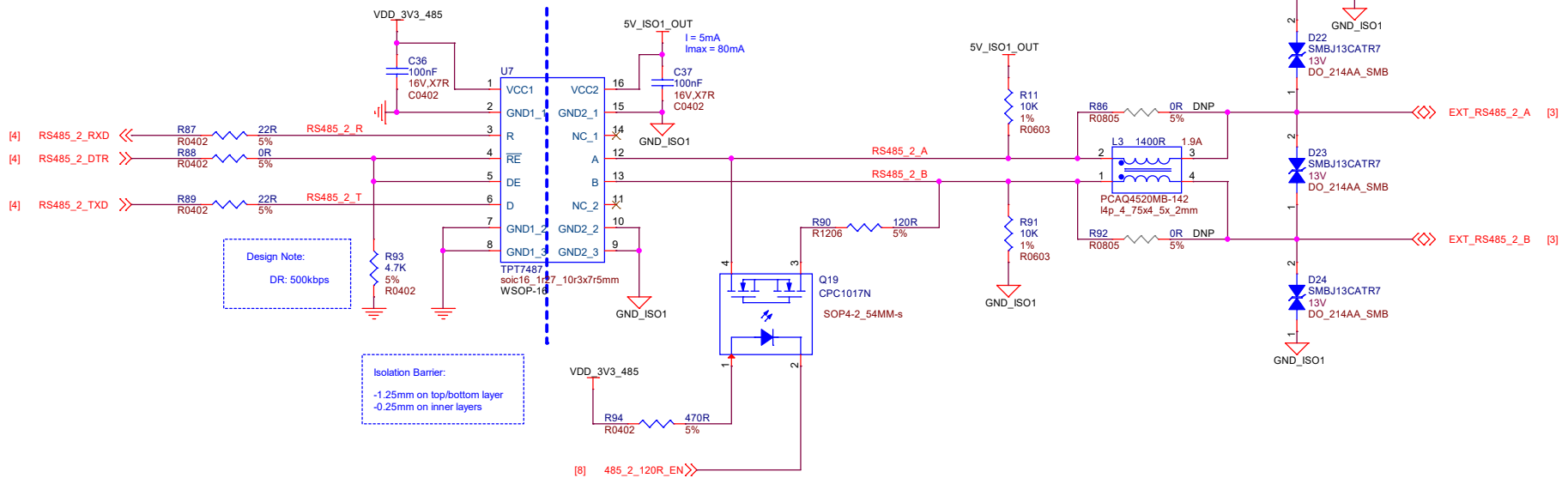
Isolated DCDC 5V to 5V

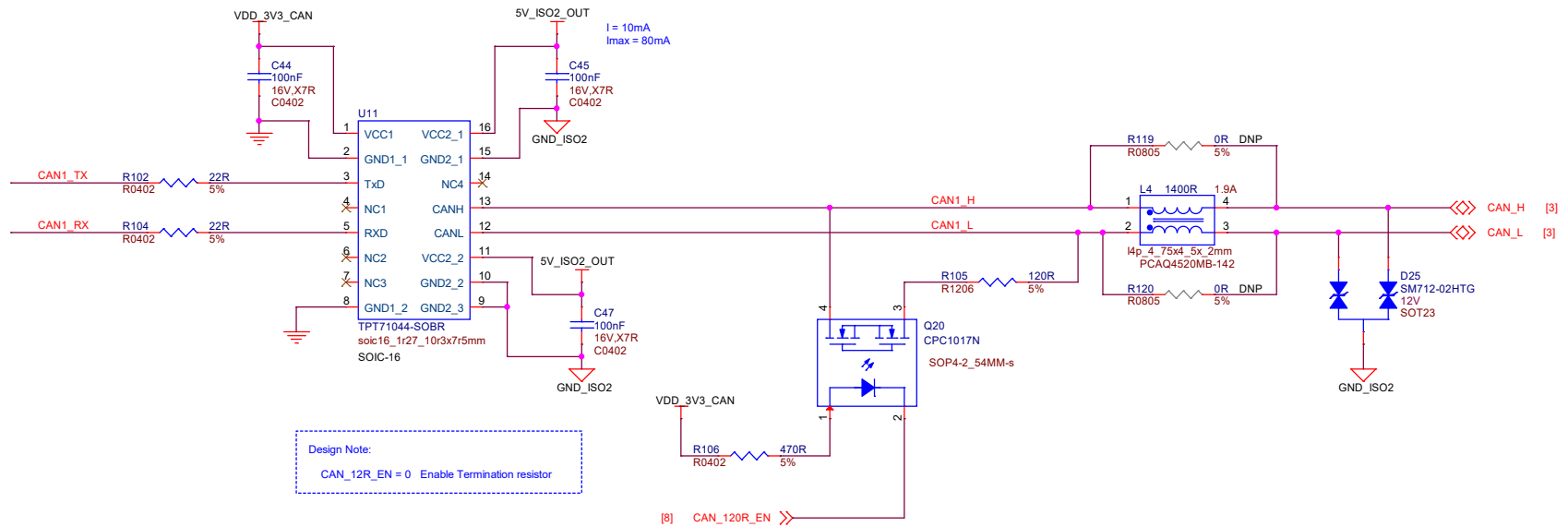
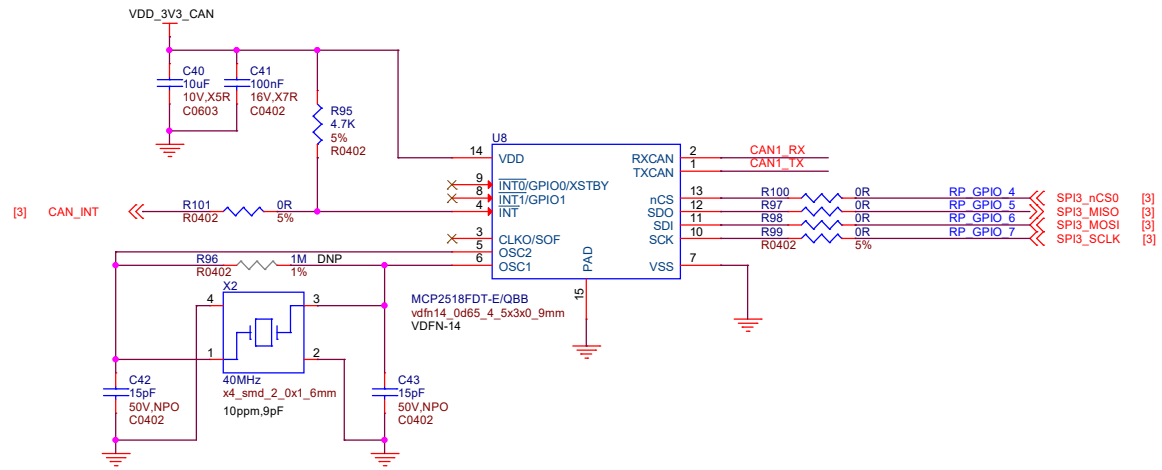
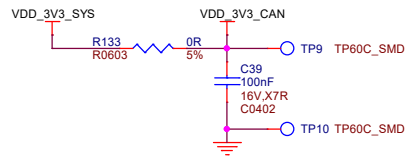
		https://www.seeedstudio.com	
		Title: reComputer R20 Exp Board	
Size: A3	Document Number: 03 Header & Power	Rev: v1.0	
Date:	Monday, November 24, 2025	Sheet: 3 of 8	

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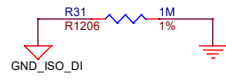
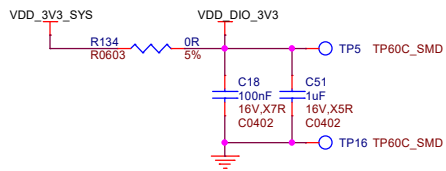


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Design Note:
CAN_12R_EN = 0 Enable Termination resistor



Design Note:

DI:	DI_1: IO_EXP_P0.0	DO:	DO_1: IO_EXP_P0.4
	DI_2: IO_EXP_P0.1		DO_2: IO_EXP_P0.5
	DI_3: IO_EXP_P0.2		DO_3: IO_EXP_P0.6
	DI_4: IO_EXP_P0.3		DO_4: IO_EXP_P0.7

